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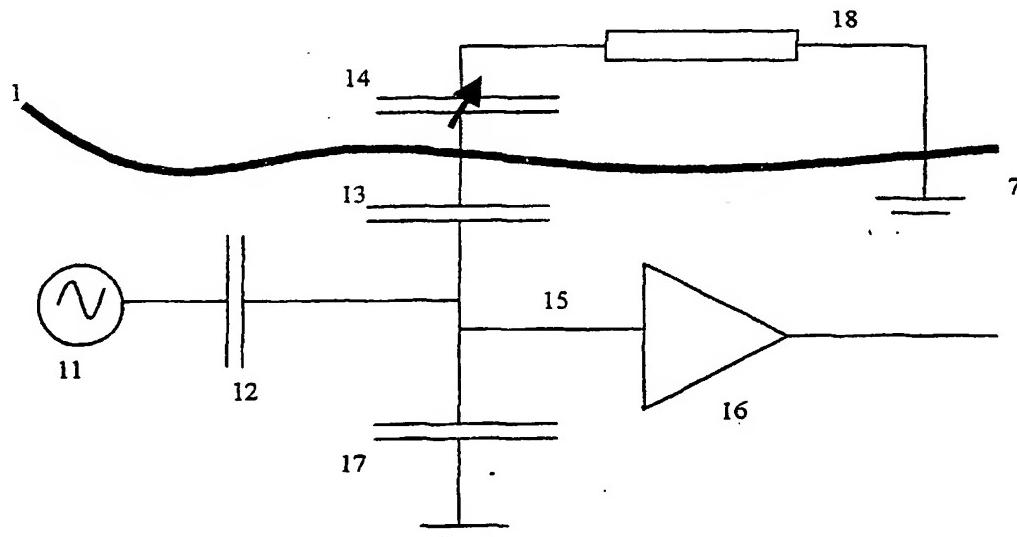
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[Continued on next page]

(54) Title: SENSOR MODULE FOR MEASURING SURFACES



(57) **Abstract:** This invention relates to a sensor module for measuring structures in a surface, especially in a finger surface being moved over the sensor module, comprising a number of sensor elements, and an outer electrode located aside the sensor elements, the sensor elements being coupled to at least one AC drive circuit providing a varying current or voltage, thus coupling a signal through the sensor elements to the outer electrode. The sensor elements are also coupled to an electronic circuit positioned on a substrate, said substrate comprising conductor leads coupling the sensor elements to the electronic circuit, and said electronic circuit being adapted to measure the magnitude of said capacitance or AC impedance.



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## SENSOR MODULE FOR MEASURING SURFACES

5 This invention relates to a sensor module being capable of sensing the topological pattern of a surface in direct contact with it, e.g a fingerprint.

The market of biometrics is evolving rapidly. However, for biometrics to penetrate the consumer market, there are strict requirements with respect to e.g. sensor price, compactness, fingerprint image quality and power consumption.

10 AC capacitive fingerprint sensors are one of the most promising technologies for realizing compact and low cost fingerprint sensors for the consumer markets, and several concepts have been proposed over the past few years. The sensor concepts roughly divide into two categories: matrix sensors, where the fingerprint is placed onto a two-dimensional sensor surface, and scanners or sweep sensors, where the user is  
15 required to pull his or her finger across the sensor to capture an image.

US Patent 6,069,070 describes a typical matrix type, AC capacitive fingerprint sensor. The sensor is basically a silicon chip (IC) which is furnished with a 2-D matrix of sensor elements ("pixels") as well as amplifiers and other circuitry. A drive electrode located on the sensor package, outside the active sensing surface, is used to couple an  
20 AC voltage into the finger. The AC signal then penetrates the finger and couples through a dielectric layer to sensor elements (pads) on the surface of the sensor. The sensor pads are coupled to amplifying circuitry in the silicon chip.

This sensor configuration however has several disadvantages: For obvious reasons, the active surface of a matrix sensor needs to be as large as the portion of the  
25 finger that shall be imaged, in other words in the order of 100 mm<sup>2</sup>. Because the price of silicon ICs increases proportionally with the chip area, such a large chip may be forbiddingly expensive for many consumer applications.

In addition, the fact that the finger is in direct contact with the surface of an active silicon IC puts a heavy demand on the materials used to protect the IC against  
30 wear, mechanical impacts, humidity, chemical compounds and electrostatic discharges (ESD). This may make it necessary with unstandard and expensive protective materials, or may lead to reduced reliability and lifetime of the sensors.

Fingerprint scanners, where the user is required to pull his or her finger across the sensor, do not have to have the same length as the fingerprint, and may therefore be smaller and more cost-effective. Because of their reduced size and lower price, scanning sensors may be a better choice for typical mass consumer applications. Scanners require 5 only a very limited number of sensor lines, and while the width of the sensor still has to match the width of the fingerprint, the sensor length may be as low as a few mm or even less. The "linear" arrangement of sensor elements also gives more flexibility in designing the interface between the sensor and the finger - it does not necessarily have to be a flat surface like matrix sensors often are.

10 Patent application PCT/NO98/00182 shows an example of a scanning sensor where the amplifying silicon IC is placed on a oriented circuit board and where the board edge constitutes the interface between sensor and finger. In this sensor, each sensor element is a fine-line trace on the board running perpendicular to the finger surface. The outer ends of the traces are covered with a dielectric layer to assure a pure capacitive 15 coupling to the finger surface. To be able to determine the speed of the finger, two lines of sensor elements are used, one on each side of the board. The speed measurement is needed for building up a 2-D image based on the readings from the sensor. Examples of other scanning sensors are given in US 6,289,114, PCT/NO01/00239 and EP 0 735 502 A2.

20 In the sensor configuration described in PCT/NO98/00182, the size of the amplifying and signal conditioning IC is linked neither to the width or length of the portion of the fingerprint to be captured. This makes it possible to design a much smaller IC chip and thus to save cost.

The described sensor however has the disadvantage that it is quite large and 25 bulky, and that the circuit board which extends in a direction perpendicularly to the finger surface gives the sensor a very high profile. For many applications, e.g mobile phones, a sensor profile of less than a few millimeters will be desired to have room for the sensor in the phone.

This problem is solved in the sensor described in patent application 30 PCT/NO01/00238. This patent application describes a fingerprint scanner where the integrated circuit with amplifiers etc (ASIC) is mounted on the back side of a substrate. The substrate is for instance silicon, ceramics or glass. The top side of the substrate is

equipped with a number of conductive pads for sensing the capacitance, and conductive vias are made through the substrate to connect each sensor pad or element with a corresponding input pad on the the ASIC. The sensor pads are covered with a dielectric material.

5        This substrate based sensor has several advantages: First of all, it maintains all the advantages of the scanner, where the size of the costly ASIC is "decoupled" from the size of the finger. In addition, the substrate, which is far less expensive to make per area, serves as a mechanical and environmental protection for the vulnerable ASIC on its back side. The sensor has a very low profile, and if supplied with e.g. BGA balls it  
10      does not have to be further packaged before mounting it onto a motherboard, e.g in a phone.

The sensor also has the advantage that drive electrodes for stimulating the finger with an AC signal can be integrated directly on the surface of the substrate. These drive electrodes may be coupled to ground through an ESD protecting semiconductor  
15      device, so that any ESD discharge from the finger will go to the electrode rather than to the sensor pads. Unlike a matrix sensor, the opening in the drive electrode around the pads can be very small, minimizing the risk of ESD induced damage even for very "localized" electric discharges.

All the three above described principles however require that the finger is  
20      stimulated directly with an AC voltage. This may not always be an advantage. For instance, the signal from the unshielded drive electrode may in some cases create electrical interference with other electronic equipment.

For the substrate based sensor described in patent application  
PCT/NO01/00238 there is yet another disadvantage: For shielding purposes it will often  
25      be necessary to place a grounded layer (or a layer at any other fixed potential) between the drive electrode and the underlying circuitry. This however means that the AC signal from the drive electrode will couple capacitively to this grounded layer and thereby increase the current consumption of the sensor. This may be a disadvantage especially  
30      for hand-held equipment such as mobile telephones where battery lifetime is of great importance. The consumed current may be particularly large for large drive electrodes, this dielectrics and high drive voltages. Similar disadvantages are related to the similar solution described in PCT/NO01/00239.

- Three possible solutions to this problem is to decrease the drive voltage, to decrease the size of the drive electrode or to increase the thickness of the dielectric between the two metal planes. All these approaches however have their drawbacks : A lower drive voltage will decrease the signal to noise ratio and limit the system's ability
- 5 to resolve certain types of fingerprints, e.g from very dry fingers. If the drive electrode is too small the signal level may also be reduced because the contact area for coupling the AC voltage into the finger is insufficient. In addition, when a finger is present the AC signal will couple from the finger to the underlying ground anyhow, regardless of the presence of the drive electrode. So the effect with respect to current consumption is
- 10 questionable. To use a thicker dielectric may be impractical from a process point of view. Hard, durable dielectric materials compatible with e.g silicon substrate technology, such as SiO<sub>2</sub> or SiN, are seldom more than a couple of microns thick. Polymeric materials such as polyimide may be thicker but are not as reliable with respect to wear.
- 15 It is therefore an object of this invention to provide a sensor module in which the external electrode in direct contact with the finger could be grounded or coupled to another fixed potential instead of carrying an AC voltage. This would also have an advantage with respect to ESD as the discharges would go directly to ground without the need for any extra protecting device, or without any risk that the driver circuit will
- 20 be damaged by such discharges.

One sensor principle where the electrode in contact with the finger is grounded is described in US patent 5,862,248. This sensor is however not a scanner and therefore suffers from the general drawbacks of matrix sensors described above.

The present invention aims at a scanning sensor principle which features low

25 cost, low current consumption, high reliability against environmental and mechanical impacts, ease of packaging and mounting, low electromagnetic radiation to the surroundings and high tolerability to ESD. The invention is characterized as described in the independent claim.

According to a preferred embodiment of this invention the substrate of the

30 sensor module is provided with a number of openings through which openings electrical contacts being coupled to the sensor elements are lead, and that the electronic circuit is positioned on the opposite side of the substrate relative to the sensor elements.

According to an alternative embodiment the module comprises at least one AC drive electrode and an array of capacitances, resistances or other impedance networks for coupling the signal on said drive electrodes to said sensor elements. The substrate is fabricated from silicon, glass, ceramics or a polymeric material.

5 According to a preferred embodiment the module comprises one or more electrodes being capable of exiting each sensor element, so that the response from each sensor element may be calibrated.

The invention is described below with reference to the accompanying drawings, illustrating the invention by way of examples.

- 10 Figure 1 illustrates a schematic circuit according to the invention.  
Figure 2 illustrates a longitudinal section of the sensor.  
Figure 3 illustrates a cross section of the invention.  
Figure 4 illustrates a sensor according to the invention as seen from above.  
Figure 5 illustrates an alternative measuring circuit with inverting amplifier  
15 Figure 6 illustrates schematically a measuring system based on the circuit in figure 5 with several sensor elements.  
Figure 7 illustrates the corss section of a detail of area around sensor element in a sensor module according to the invention.

In one preferred embodiment, detailed in figure 2, 3 and 4, the invention  
20 consists of a substrate 2 which may be fabricated from e.g silicon, glass, ceramics, flexprints or a laminate board. On the top side of the substrate, where the finger to be imaged 1 shall be pulled across, a number of sensor elements 5 are defined, e.g as conductive pads made in a planar process. The sensor elements may either be covered with a dielectric such as SiO<sub>2</sub> or SiN or be in galvanic contact with the finger. The  
25 sensor elements are preferably arranged in a single line with additional sensor elements 6 for detecting the speed and/or the direction of movement, including rotation, of the finger across the sensor. These movement measurements may either be used for correcting the image with respect to variances in finger speed, pull direction or rotation, or e.g to move a cursor across a screen ("mouse").

30 The top surface of the sensor preferably contains a grounded electrode 7, preferably in direct contact with the surroundings, which covers an at least significant

part of the substrate surface. Alternatively the electrode may be covered with a dielectric material 8.

The substrate further contains a number of via holes 3 through which connections from the sensor pads 5 are taken through the substrate 2 to the back side.

- 5 The via holes 3 are generated by laser or mechanical drilling or an etching process and must subsequently be electrically isolated from the substrate, if the latter is conductive. This may be done by e.g oxidation or deposition techniques. If necessary, additional routing is included between the bottom end of the via holes and the substrate bonding pads of an ASIC 4 which is mounted on the back side of the substrate (e.g flip-chip or  
10 wire bonded). Alternatively, there may be routing on top of the substrate as well, preferably in an additional layer below the grounded electrode 7. In case of silicon or any other conducting or semiconducting substrate type, the substrate 2 is at a fixed potential.

15 The ASIC contains a number of amplifiers 16 to amplify the signal associated with each imaging channel (sensor element), as well as other signal conditioning circuitry.

- Finally, the sensor contains one or several drive circuits 11 that may be used to couple an AC signal (current or voltage) to each sensor pad node 15. From each node, this signal will couple through the sensor dielectric (if any), through the finger and to  
20 the grounded electrode 7 on the sensor surface. These drive circuits may for instance be realized e.g as constant amplitude AC current sources. The current sources may be coupled to the sensor pad nodes 15 either directly or through an impedance network. Alternatively the drive circuits may be realized as AC voltage electrodes coupled to the individual channels (sensor pads) through capacitances or other impedance networks.  
25 The drive circuits may either be located in the circuitry on the ASIC or on any side of the substrate. AC voltage electrodes with a pure capacitive coupling, as shown in figure 1, may for instance be obtained by fabricating plate capacitors in a two-layer metallization process. To minimize current consumption it is however important that the parasitic coupling from these drive electrodes to ground is as small as possible.  
30 Alternatively, a resistive coupling or more complicated impedance networks may be used.

- A schematic version of a preferred measuring principle is shown in figure 1. An AC voltage signal 11 from the drive electrode couples to the sensor pad / amplifier input node 15. In an alternative embodiment the combination of 11 and 12 may be replaced by for instance a constant amplitude AC current source. In the case shown in 5 figure 1, the input signal voltage to the ASIC amplifier 16 is given by the voltage division between the capacitor 12 and the combined capacitance of the capacitor to ground 17 in parallel with the series capacitance of 13 (through the sensor dielectric 8), and 14 (through the finger ridge or through an air gap in a valley). The additional series impedance 18 through the finger is for this discussion assumed to be negligible.
- 10 Because all other elements are fixed, the magnitude of the input signal will change dependent on the magnitude of the capacitance 14, which varies depending on whether there is a finger ridge or valley present. The voltage input signal at 15 may be amplified e.g by a non-inverting voltage amplifying circuit.

To maximize sensitivity, the magnitude of the capacitor 12 should be  
15 comparable to capacitor 17. In some cases capacitor 17 is dominated by the input capacitance of the ASIC amplifier, and may therefore be of a certain minimum size (for instance a few pF) for practical reasons. In this case the capacitor 12 will also have to be comparably large. In the general case 17 does not have to be a capacitor, but can be any kind of impedance network or other circuitry with a controlled relationship between  
20 current and voltage.

The invention is however not limited to the coupling described in fig 1, and other schematics, including current sensing techniques may be used to measure the magnitude of the variable capacitor 14, which is the main measurand.

An alternative measuring principle is shown in figure 5. Here an inverting  
25 amplifier is employed to measure the current flowing from the drive electrode to the virtual ground terminal of the amplifier 16 through the capacitances 12 and 17. The output voltage from the amplifier is given by the voltage drop of said current across the feedback capacitor 19.

The advantage with this principle is that the input capacitance of the ASIC  
30 does not impact on the measurements. This means that the capacitance 12 does not have to be comparable to the ASIC input capacitance, but only to the capacitance 17 which can be made arbitrarily small.

Figure 7 shows an example of how the principle of figure 5 can be implemented in a three layer metallization process on the sensor surface. Here the drive electrode and signal track to the ASIC are fabricated in a metal layer directly below the sensor elements, and separated from the latter by a dielectric material to achieve a  
5 capacitive coupling between the sensor elements and said electrodes and tracks.

Figure 6 shows a situation where several sensor elements 5a – 5d are coupled to the same sensor input channel. The channels have separate drive electrodes 11a – 11d that can be operated independently. It can be seen from figure 7 that only those sensor elements with an “active” drive electrode (i.e a drive electrode with an AC voltage) will  
10 contribute to the signal current. This arrangement thus makes it possible to “multiplex” the signal from the various sensor elements on the substrate using the signal on the drive electrodes as “control signal”. This may be an advantage as it allows to switch between several sensor elements using the same ASIC input channels, so that the number of channels can be reduced.  
15

As is evident from the drawings the sensor module according to the invention may include electronic circuitry comprising input means including at least two amplifier circuits 16, each amplifier circuit being coupled to a sensor element set 5a-5d including at least two sensor elements for amplifying the signals therefrom. Each sensor element 5a-5d in each set being coupled to a different drive unit 11a-11d for exciting each  
20 sensor element according to a predetermined sequence. The amplifier circuit thus receiving a signal from only one sensor element in said set at the time, which thus provides a multiplexed signal being transmitted to the electronic circuitry.

The abovementioned PCT/NO01/00238, which describes the preferred method for producing the sensor module according to the invention requires that a large number  
25 of routing tracks from the sensor elements are taken through the substrate and connected to the ASIC. Several problems are related to this solution: First of all, the employed substrate technology must allow for a large number of through-going conductive vias within a relatively small area. This excludes the use of a number of different low-cost substrate processes. As an example, present day technology for both ceramic, glass and  
30 laminate substrates may have problems with the requirement for via density.

The large number of connections also in practice excludes the possibility of taking the connections out on the side of the sensor using wire bonds.

In addition, there needs to be one ASIC input channel, and possibly even one preamplifier, for each sensor pad on the top surface. As both the i/o pad, amplifier and related ESD protection circuitry consumes space on the ASIC, it will be difficult to reduce the size of the chip below a certain limit. This may make it difficult to reach the 5 extremely low production costs needed for a mass produced sensor.

The embodiment of the invention referred to in figure 6 provides a sensor module in which the number of needed connections between the sensing surface and the ASIC, and the number of input channels on the ASIC, is significantly reduced. This will lead to a simpler substrate and reduce the size of the ASIC, thus reducing the production 10 cost of the sensor. Moreover, the number of channels are reduced by providing a form of sensor element "multiplexing" which is carried out on the substrate, and which preferably does not employ active switches or other semiconductor based components.

To calibrate the response from the sensor, a calibrating capacitor can be coupled between 15 and another AC electrode that can be turned on and off. When the 15 electrode is turned on, the change in input signal level at the input 15 will represent the overall variations in the magnitude of 12, 17 and 13, as well as the gain of the amplifier. This reading can therefore be used to normalize the output from each channel. For the calibration to be less sensitive to the presence of a finger pattern during calibration, 17 should preferably be large compared to 13. Alternatively an AC current source may be 20 used for calibration.

The present invention combines many of the advantages of the sensor technologies described in PCT/NO01/00238, US 5,862,248 and PCT/NO98/00182, while reducing or eliminating some of the drawbacks. As an example, the proposed sensor can be an essentially linear scanning sensor with a small size and low cost, 25 unlike the sensor proposed in US 6,069,070 or US 5,862,248. Unlike the sensor in PCT/NO98/00182 it has a very low profile, and unlike the sensor in PCT/NO01/00238 there is no extra current consumption linked with driving an external drive electrode or a finger in close contact with a grounded layer. It also eliminates the need for an active drive electrode in direct contact with the finger as proposed in both PCT/NO01/00238, 30 US 6,069,070 and PCT/NO98/00182. If the external electrode is grounded, this will make use of additional ESD protection circuitry less necessary as discharges from the

6. Sensor module according to claim 1, in which the module comprises one or more electrodes being capable of exiting each sensor element, so that the response from each sensor element may be calibrated.
- 5 7. Sensor module according to claim 1, wherein the substrate is a semiconductor, and where electronic circuits are defined in the top surface of the substrate.
8. Sensor module according to claim 1, wherein the drive circuits are AC current sources.
- 10 9. Sensor module according to claim 1, wherein said electronic circuitry comprises input means including at least two amplifier circuits, each amplifier circuit being coupled to a sensor element set including at least two sensor elements for amplifying the signals therefrom and transmitting it to said electronic circuitry, each 15 sensor element in each set being coupled to a different drive unit for exciting each sensor element according to a predetermined sequence, the amplifier circuit thus receiving a signal from only one sensor element in said set at the time.

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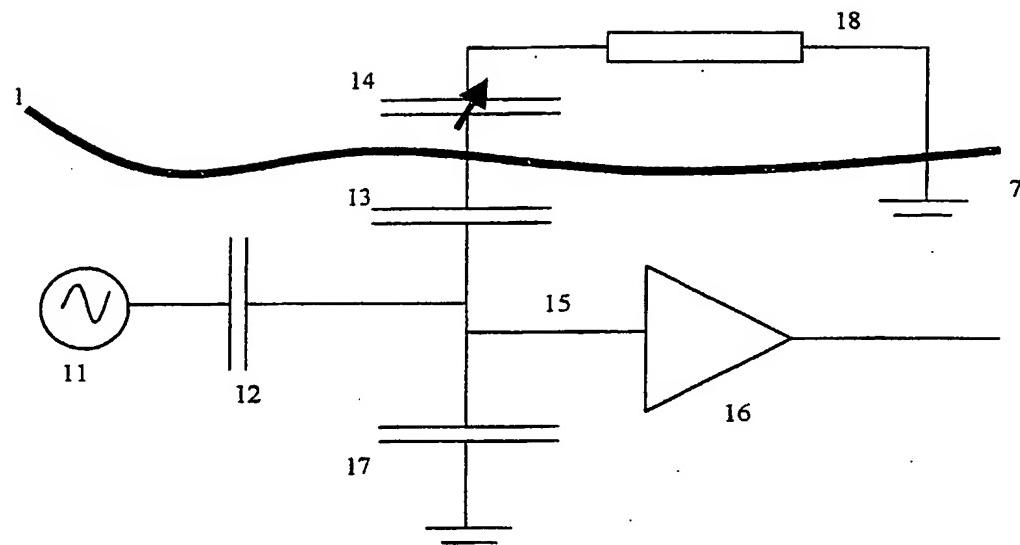


Fig. 1

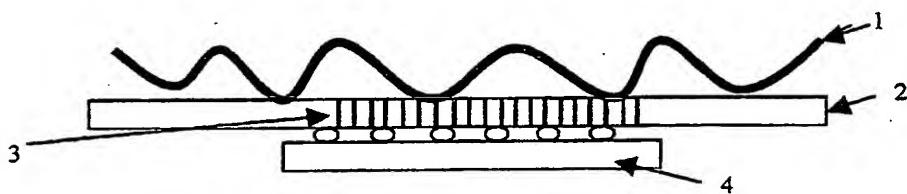


Fig. 2

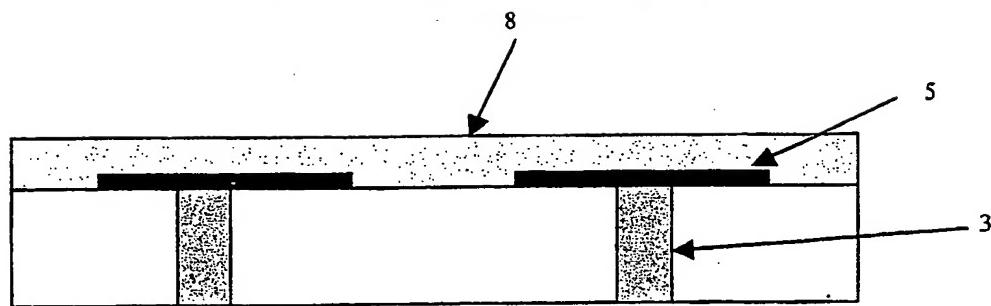


Fig. 3

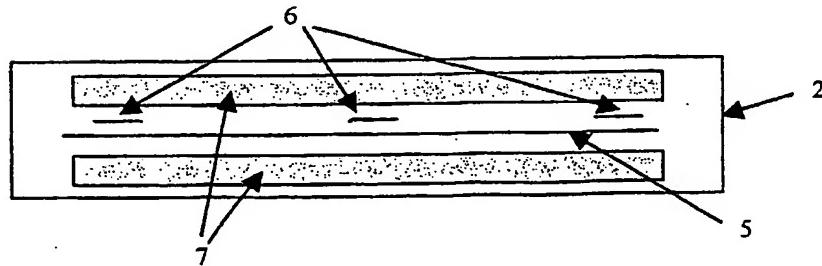


Fig. 4

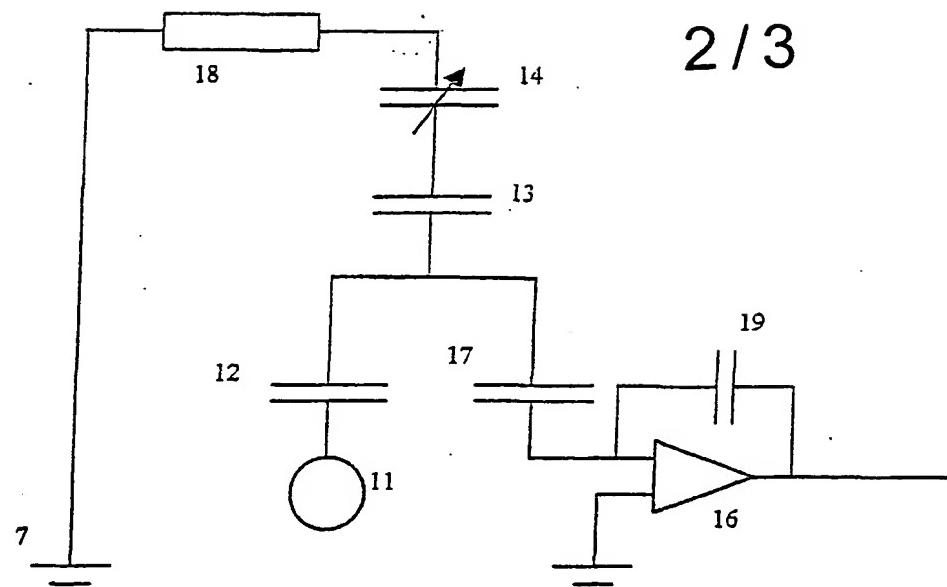


Fig. 5

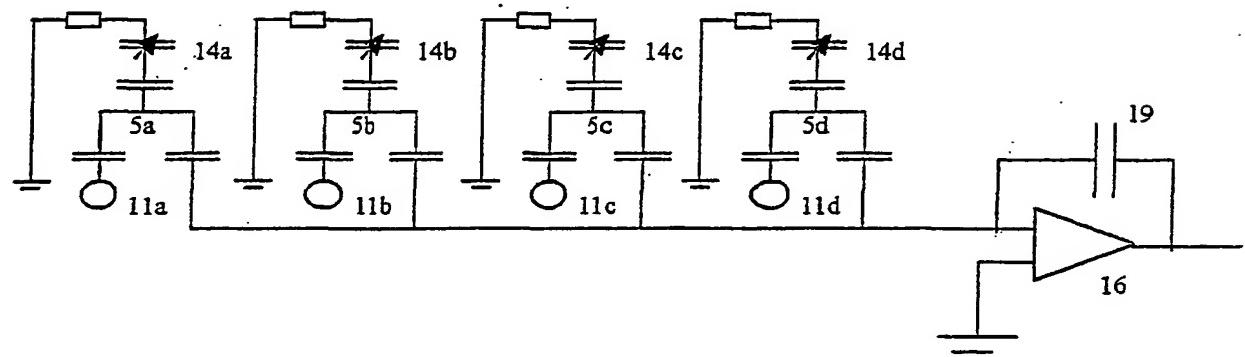
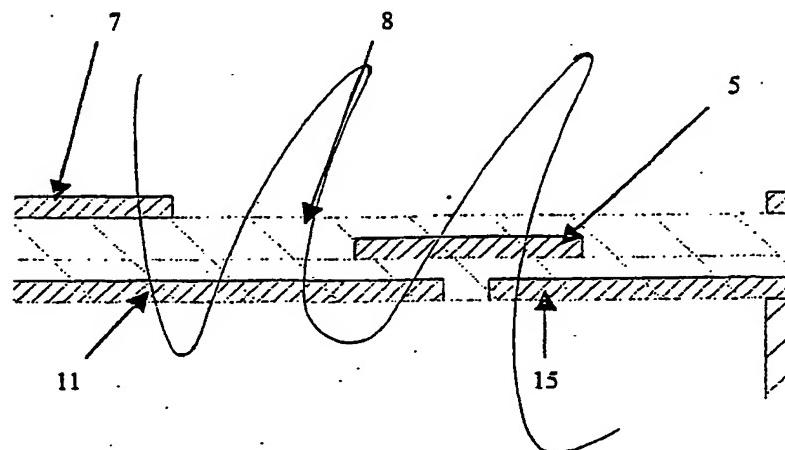


Fig. 6



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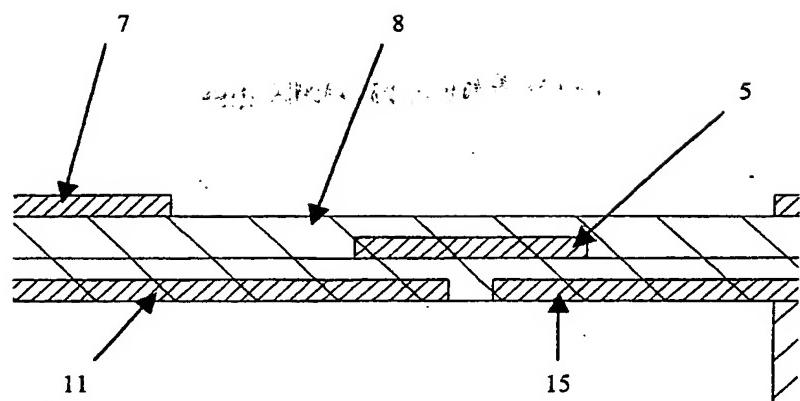


Fig 7

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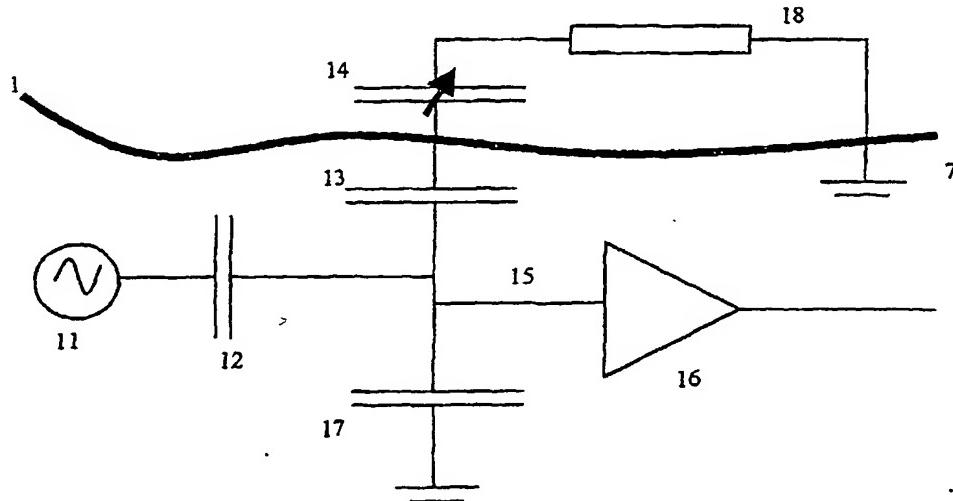
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(54) Title: SENSOR MODULE FOR MEASURING SURFACES



**WO 2003/075210 A3**

(57) Abstract: This invention relates to a sensor module for measuring structures in a surface, especially in a finger surface being moved over the sensor module, comprising a number of sensor elements, and an outer electrode located aside the sensor elements, the sensor elements being coupled to at least one AC drive circuit providing a varying current or voltage, thus coupling a signal through the sensor elements to the outer electrode. The sensor elements are also coupled to an electronic circuit positioned on a substrate, said substrate comprising conductor leads coupling the sensor elements to the electronic circuit, and said electronic circuit being adapted to measure the magnitude of said capacitance or AC impedance.



VC, VN, YU, ZA, ZM, ZW, ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG)

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## INTERNATIONAL SEARCH REPORT

International Application No

PCT/NO 03/00071

A. CLASSIFICATION OF SUBJECT MATTER  
 IPC 7 A61B5/117 G06K9/00 H01L23/48

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)  
 IPC 7 A61B G06K H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, PAJ

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	WO 01 94902 A (INDEX AS ; NYSAETHER JON (NO); TSCHUDI JON (NO); VERMESAN OVIDIU (NO) 13 December 2001 (2001-12-13) abstract; claims 1,2,6-8; figure 1	1,3-8
Y	WO 01 99035 A (SEEBERG BJOERN ERIK ; INDEX AS (NO); NYSAETHER JON (NO); VERMESAN OV) 27 December 2001 (2001-12-27) page 7, line 10 - line 31 page 8, line 28 - line 33 page 11, line 20 - line 36 abstract; claim 1; figures 1,3 ---	2,9 2

 Further documents are listed in the continuation of box C. Patent family members are listed in annex.

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Date of the actual completion of the International search

15 May 2003

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## INTERNATIONAL SEARCH REPORT

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## C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	WO 01 99036 A (IDEX AS ; NYSAETHER JON (NO); TSCHUDI JON (NO); VERMESAN OVIDIU (NO) 27 December 2001 (2001-12-27) page 6, line 29 -page 7, line 3 abstract; figure 1 -----	9

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## INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No

PCT/NO 03/00071

Patent document cited in search report		Publication date		Patent family member(s)		Publication date
WO 0194902	A	13-12-2001	NO	20003005 A		10-12-2001
			AU	6443101 A		17-12-2001
			EP	1292227 A2		19-03-2003
			WO	0194902 A2		13-12-2001
<hr/>						
WO 0199035	A	27-12-2001	NO	20003004 A		10-12-2001
			AU	6442901 A		02-01-2002
			EP	1303828 A2		23-04-2003
			WO	0199035 A2		27-12-2001
<hr/>						
WO 0199036	A	27-12-2001	NO	20003003 A		10-12-2001
			AU	6443001 A		02-01-2002
			EP	1303829 A2		23-04-2003
			WO	0199036 A2		27-12-2001
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